

FIG. 3 LOGIC FLOW FOR AUTOMATIC MAPPING

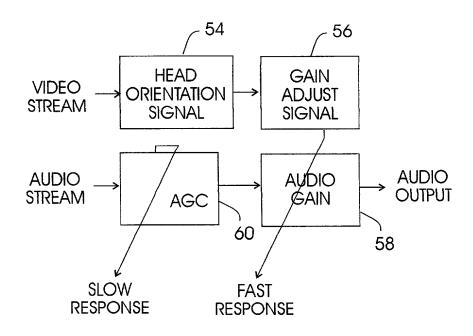


FIG. 4 DUAL GAIN ADJUST BLOCK DIAGRAM